

# Notice of References Cited

Application/Control No.

09/635,507

Applicant(s)/Patent Under  
Reexamination  
RAMKUMAR ET AL.

Examiner

Matthew A. Anderson

Art Unit

1765

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## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6150234	11-2000	Olsen et al.	438/424
	B	US-			
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	D	US-			
	E	US-			
	F	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

## FOREIGN PATENT DOCUMENTS

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## NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, USA, pp. 218-219, 228, 1986.
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	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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